CS 3853 Section 2 CPU Project: Final Report

**Group 2**

Daniel Garcia

Faisal Khurram

Fuzail Gilani

Lap Chan

Lucas Tiedeman

Michael Swan

Tyler Worsch

**What works:**

Nothing, really. We just have some skeleton code that compiles. This was definitely a project where there are critical points of failure, such as the fetch, decode, execute loop that feeds instructions from the program to the hardware.

However, we were able to create working hardware for the ALU and Memory Unit. The main issue with this was that our schedules were hectic and coordination was difficult since there was trouble with the notifications in the communication program we used (Discord), and there was trouble with familiarity with the software (not all of our group knew how to use git or GitHub).

**Who did what:**

**Daniel Garcia:** ALU

**Faisal Khurram:** Memory Unit

**Fuzail Gilani:**

**Lap Chan:** Memory Unit

**Lucas Tiedeman:** Control Unit

**Michael Swan:** ALU

**Tyler Worsch:** Memory Unit

**Final Remarks:**

Considering the amount of time that most group members put into this project, I would say that it was mildly successful. The instructions don’t work (there wasn’t a framework set up for that), but most of the hardware is there and unit tested. Any code submitted is signed by the author.

**Links:**

**GitHub**: https://github.com/DanielGarcia96/CPU\_Project